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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Jie Xue, Zhiping Yang and Yida Zou  
Serial No.: 10/792,101  
For: TECHNIQUES FOR PROVIDING EMI SHIELDING WITHIN A  
CIRCUIT BOARD COMPONENT  
Filing Date: March 3, 2004  
Examiner: Vortman, Anatoly  
Art Unit: 2835  
Conf. No.: 4081

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Date: March 8, 2007

By: Robin L. Wilkes

(Typed or printed name of person mailing  
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Signature: 

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**MAIL STOP AF**  
Commissioner for Patents  
PO Box 1450  
Alexandria, Virginia 22313-1450

**AMENDMENT**

Sir:

In response to the Final Office Action mailed January 8, 2007, please amend the above-identified Application as follows: